

FIG.1

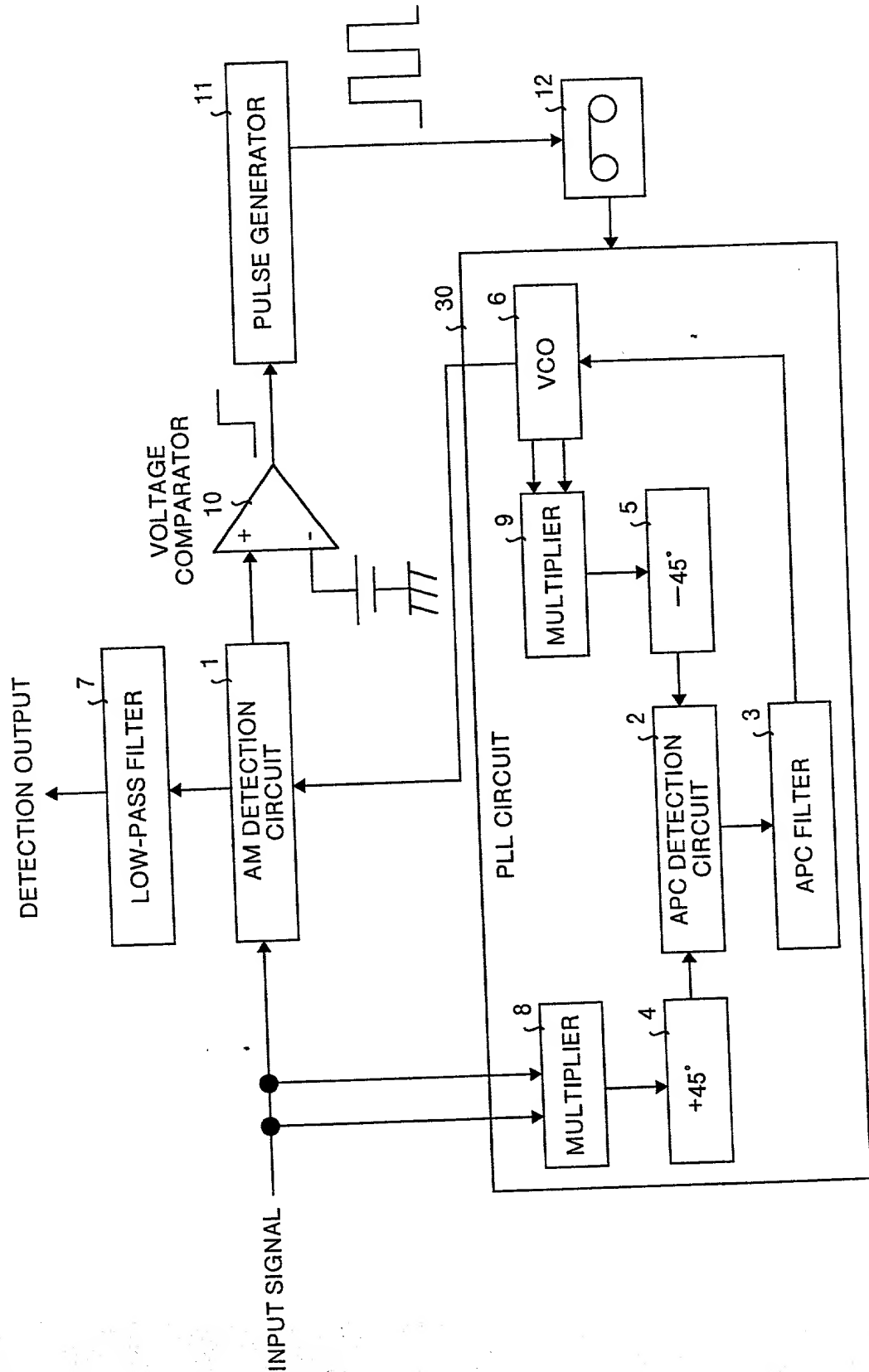


FIG.2A

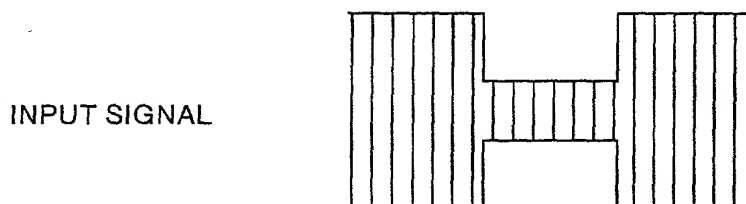


FIG.2B

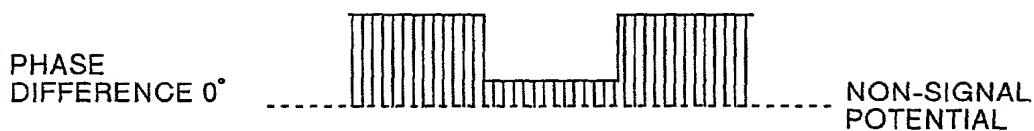
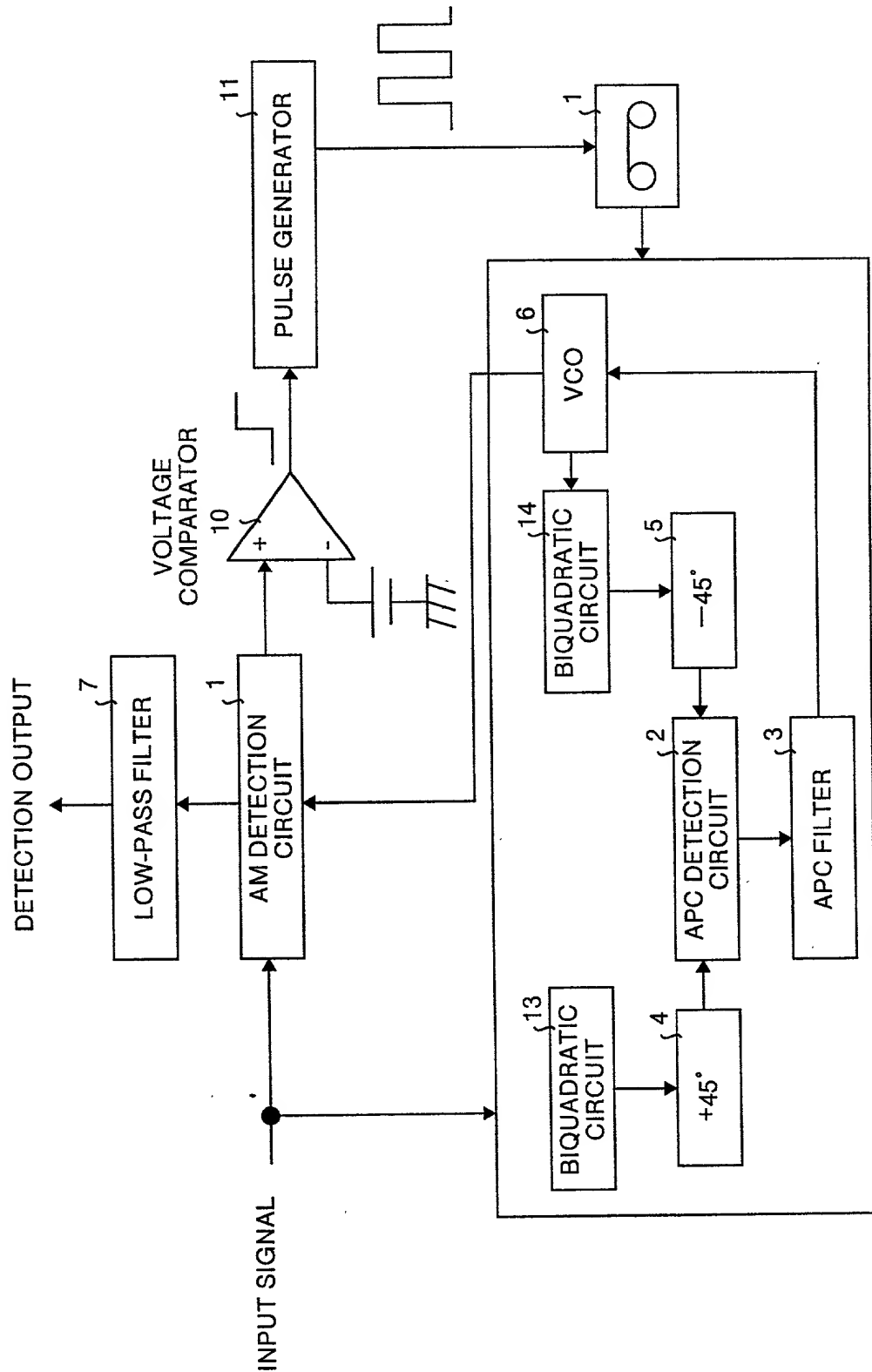


FIG.2C

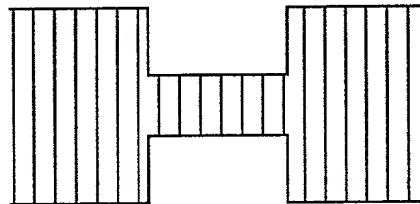


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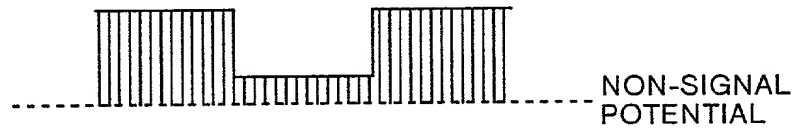
FIG.3



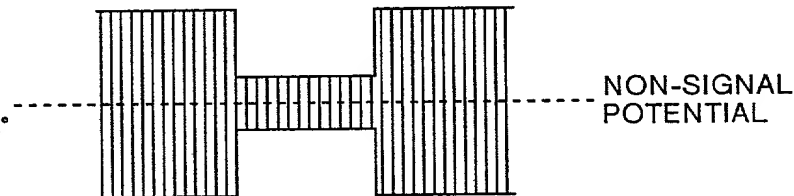
INPUT SIGNAL



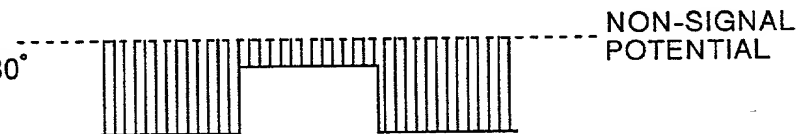
PHASE  
DIFFERENCE 0°



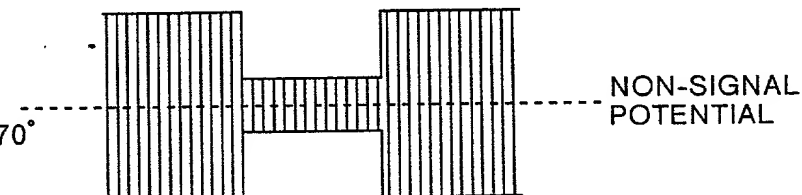
PHASE  
DIFFERENCE  $90^\circ$



PHASE  
DIFFERENCE  $180^\circ$



PHASE  
DIFFERENCE  $270^\circ$

[illegible]

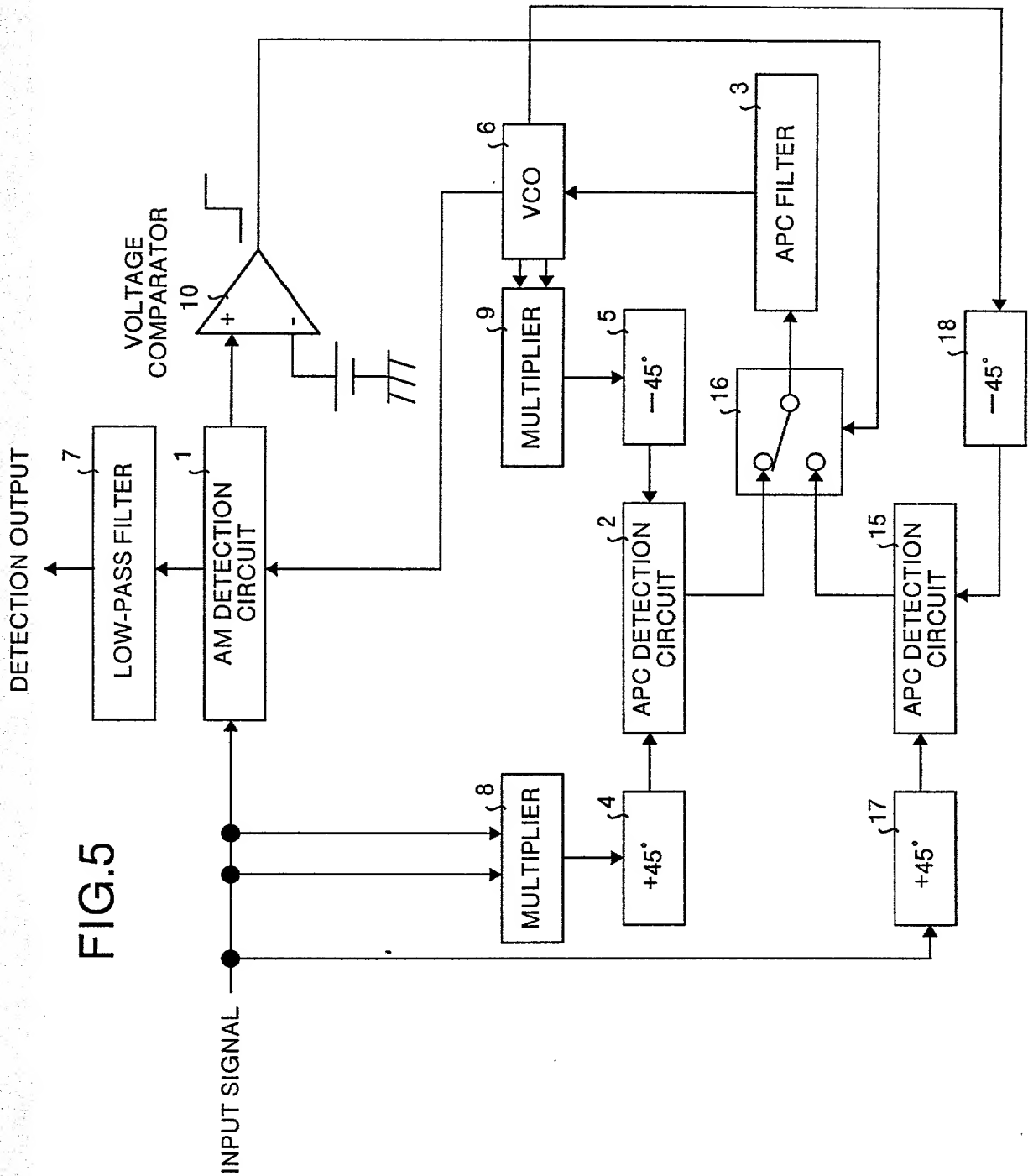


FIG.5

FIG. 5

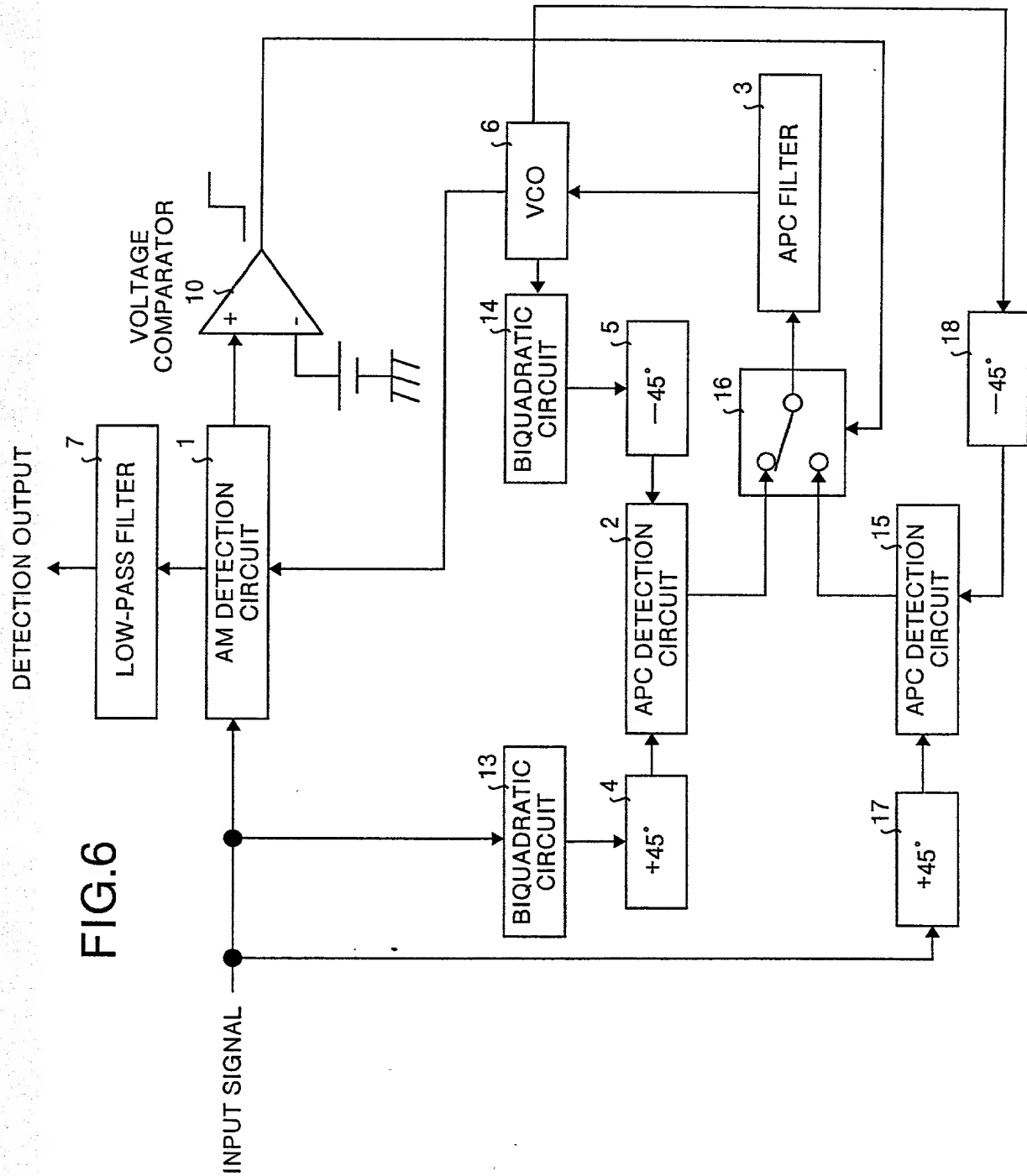


FIG.6

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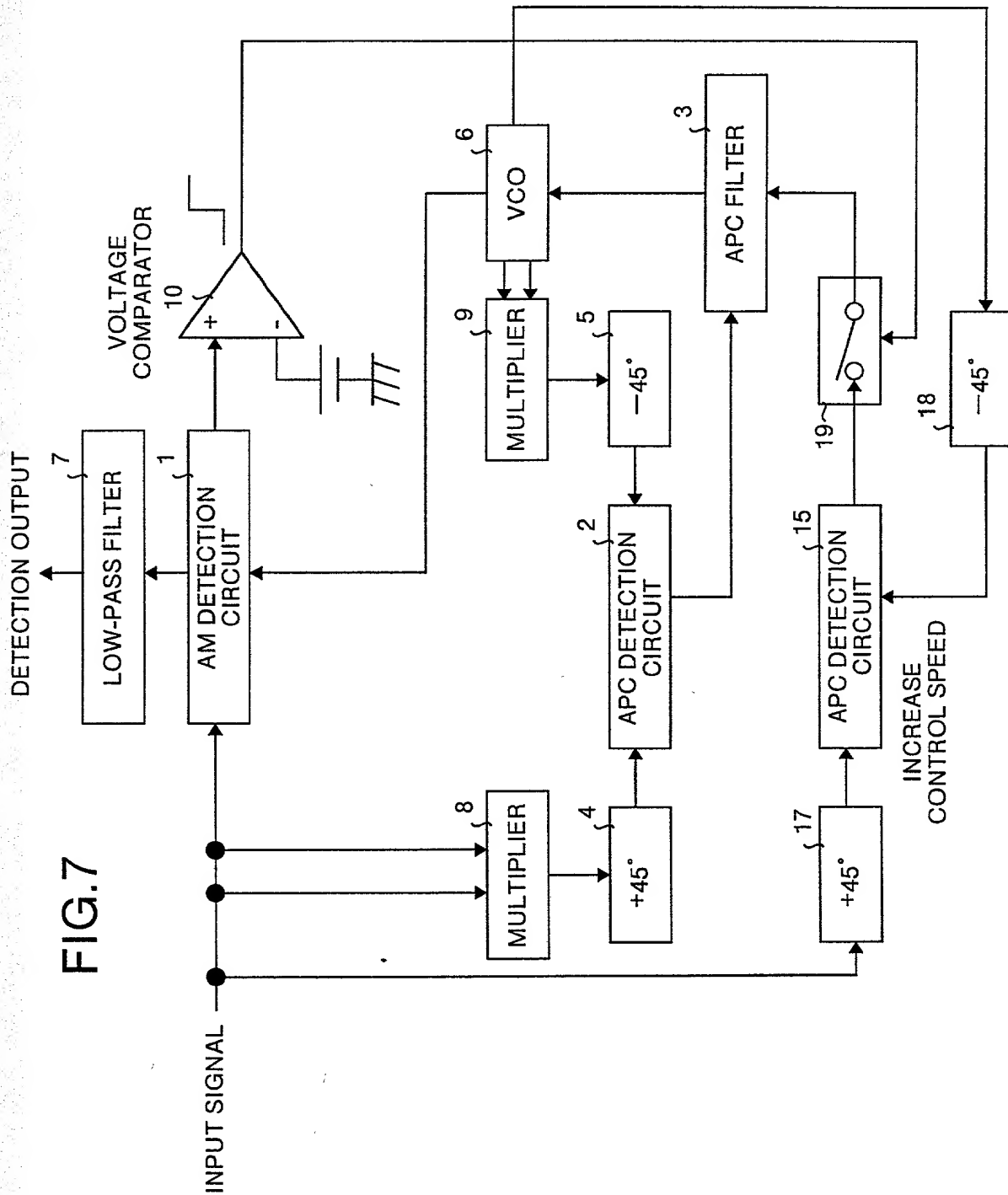


FIG. 7

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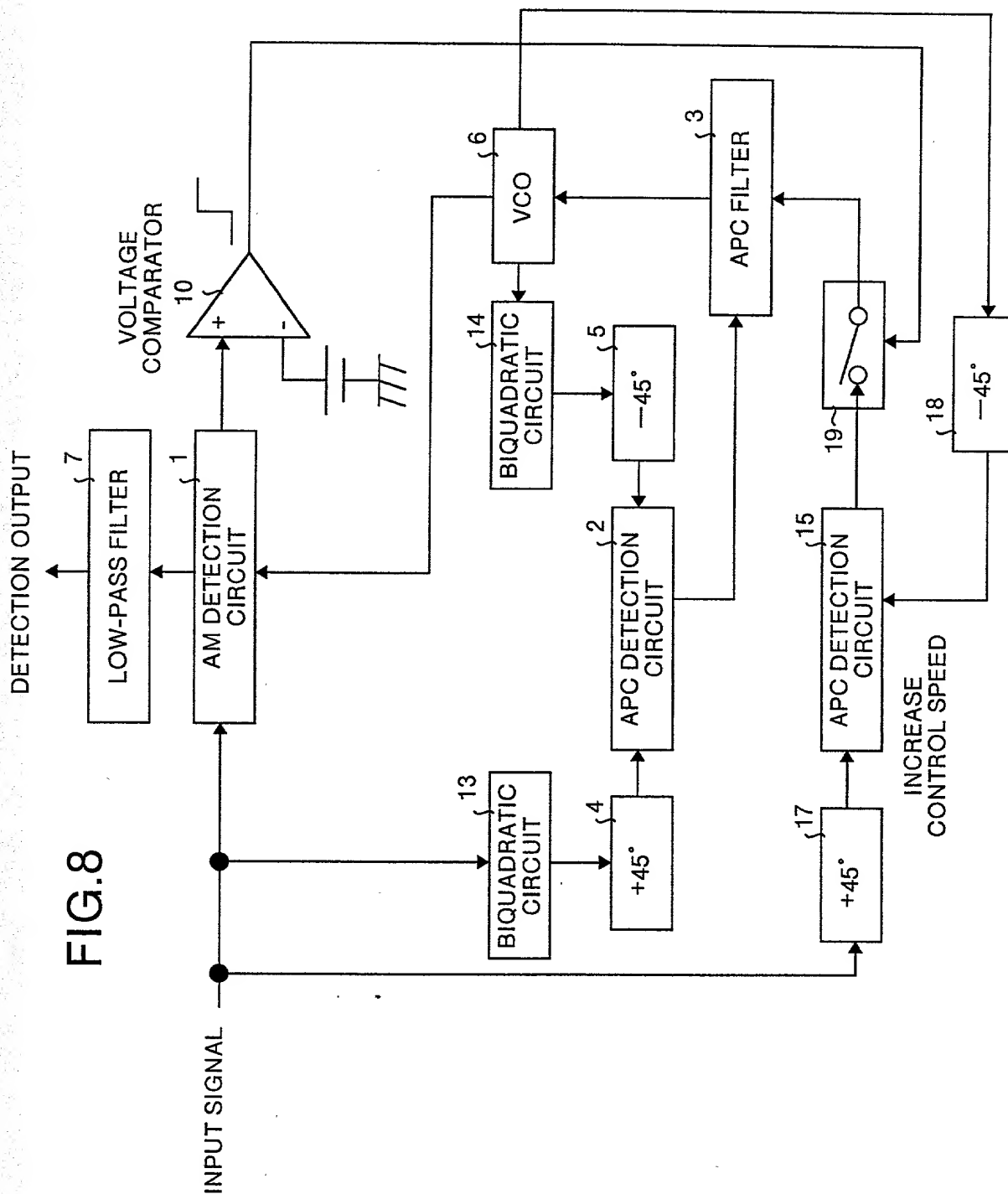


FIG. 8

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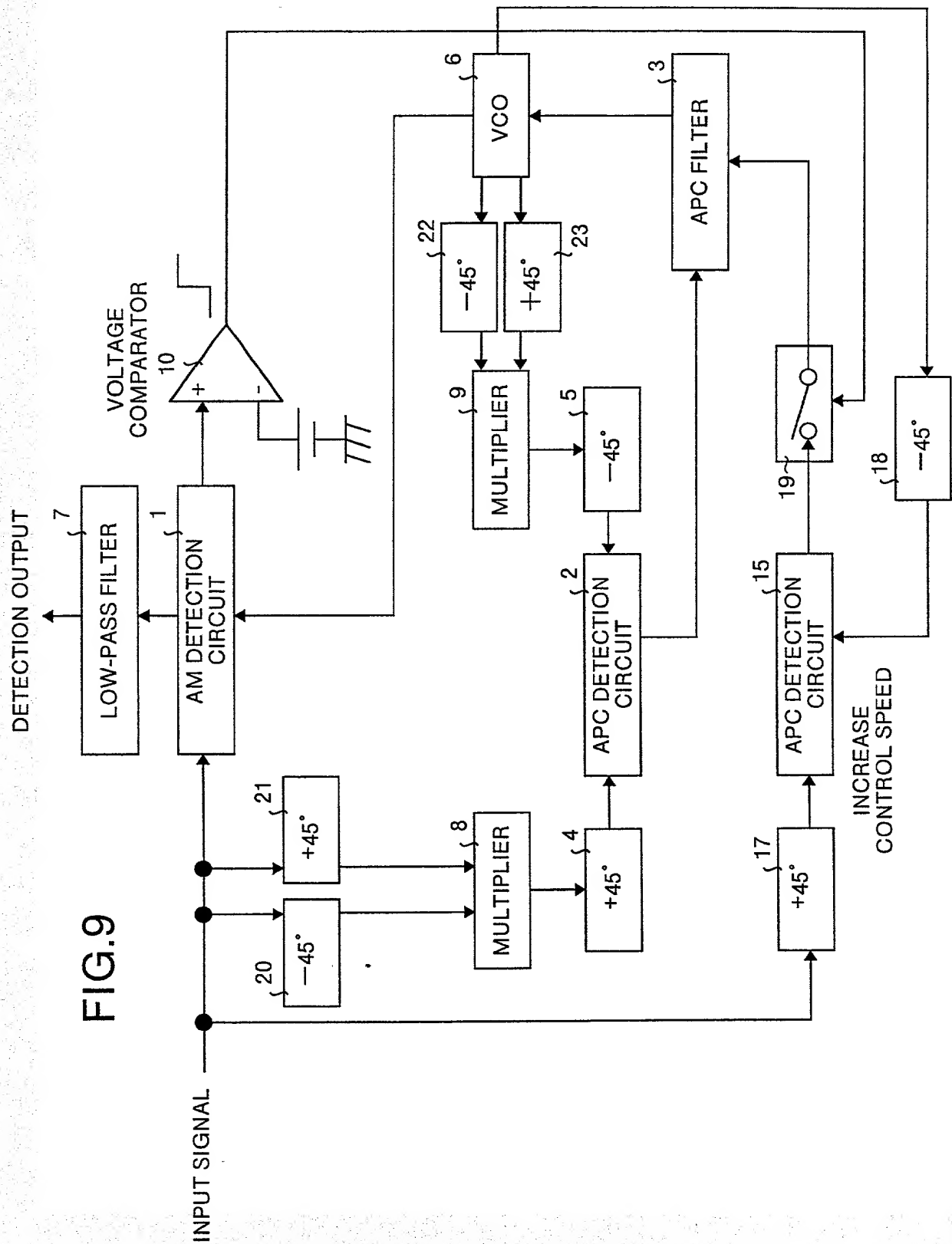


FIG. 9

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FIG.10

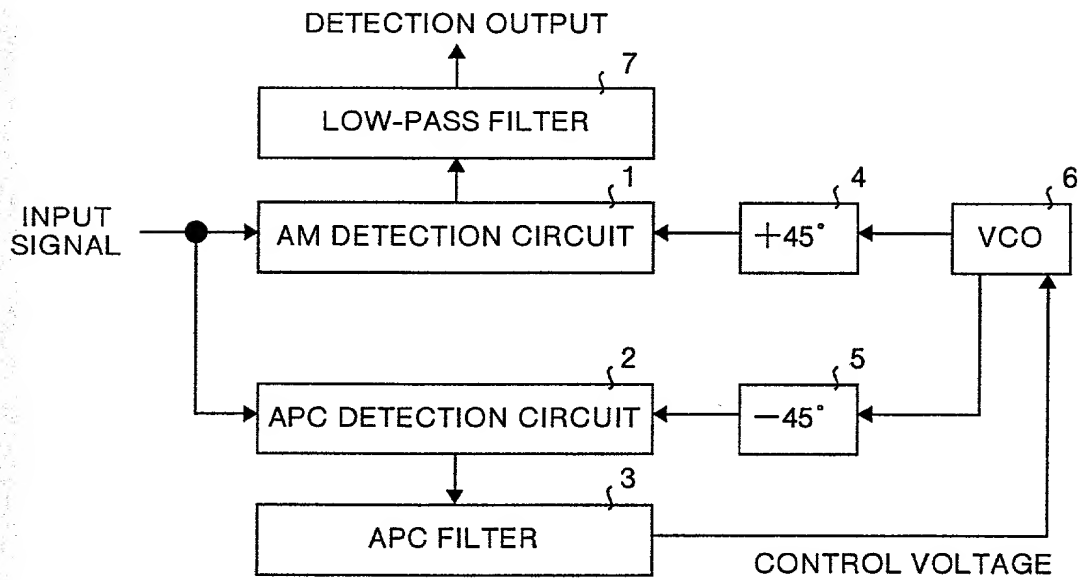


FIG.11A

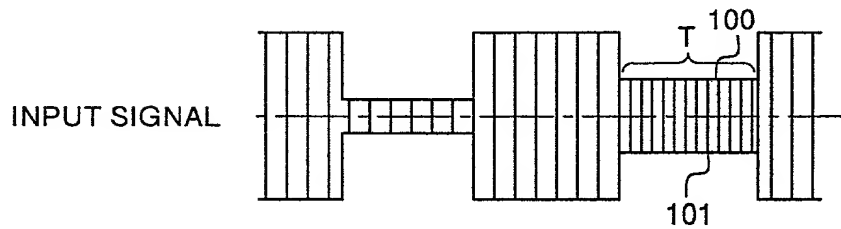


FIG.11B

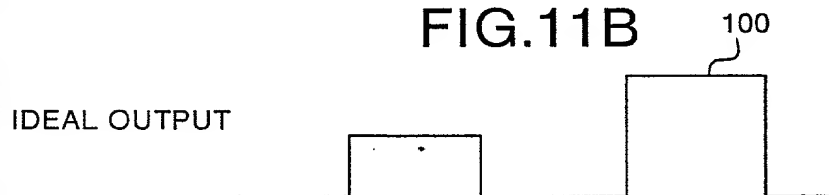


FIG.11C

